

**Abstract**

**Circuit Arrangement**

The invention relates to a circuit arrangement. The circuit arrangement contains a flip-flop having a plurality of storage transistors having a threshold voltage of a first value, and contains a power switch transistor having a threshold voltage of a second value, which is set up in such a way that, by means of the application of a predeterminable electrical potential to its gate terminal, the circuit arrangement can be brought to an operating state in which electric charge carriers contained in the circuit arrangement, in the event of at least one supply voltage being switched off, are protected against flowing away from the circuit arrangement. Furthermore, the circuit arrangement contains a plurality of switching transistors having a threshold voltage of a third value between the flip-flop and the power switch transistor, for coupling a flip-flop input signal into the flip-flop. The first and/or the second value are/is greater than the third value in terms of magnitude.